



Open PhD position “Deep learning and optimization for semiconductor test”

Start: Begin 2023

Area

Recently, intelligent machine learning methods, in particular deep learning methods, are becoming increasingly important for semiconductor test. The Graduate School Intelligent Methods for Test and Reliability ([GS-IMTR](#)) at University Stuttgart, in cooperation with the world-leading company [Advantest](#) in semiconductor test, studies topics such as design for test and diagnosis, post silicon validation, test generation and optimization, robust device tuning, system-level test, lifetime test, and test automation.

Topic

This open PhD position is located in Phase 2 of GS-IMTR. It will start at beginning of 2023 and has a duration of 3 years. It will continue the successful project “Deep learning based variable selection for post silicon validation” in Phase 1 of GS-IMTR. The main topic is to study methods for variable selection with automated feature design based on deep learning and optimization for semiconductor test.

Requirements:

- High interest on this topic
- High–performance Master degree in related areas (e.g. EE, CS, ..)
- Solid knowledge in deep learning and experience in Python programming
- Solid knowledge in optimization
- Interest on teamwork

In case of interest, please contact Prof. Bin Yang (bin.yang@iss.uni-stuttgart.de) by sending complete CV and transcripts of Bachelor and Master.